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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of

KIMURA et al.

Serial No. 10/668,166

Filed: September 24, 2003

For: SEMICONDUCTOR DEVICE AND CHIP-STACK  
SEMICONDUCTOR DEVICE



Atty. Ref.: 1035-473

TC/A.U.: 2811

Examiner: ARENA, Andrew O.

\* \* \* \* \*

August 27, 2008

Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**RESPONSE TO NOTICE OF NON-COMPLIANT APPEAL BRIEF**

This is in reply to the above-captioned Notice dated August 15, 2008 indicating the Appeal Brief filed July 2, 2008 is deficient/non-compliant in that “the brief does not contain a statement of the status of all claims, (e.g., rejected, allowed, withdrawn, objected to, canceled) or does not identify the appealed claims (37 CFR 41.37(c)(1)(iii)).” In particular, the Notice indicates “the appealed claims have not been identified in the status of claims section of the brief.”

Attached please find a revised “Status of Claims” section of Applicant’s July 2, 2008 Appeal Brief including the status of all claims and properly identifying the appealed claims.

KIMURA et al.  
Serial No. 10/668,166  
August 27, 2008

This response is timely filed. Please proceed to process the Notice of Appeal filed July 2, 2008 and with the examination of this application.

Respectfully submitted,

**NIXON & VANDERHYE P.C.**

By: \_\_\_\_\_

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**STATUS OF CLAIMS**

Claims 1-2, 4-5, 7-9, 11-13, 15-17, and 19-20 are pending and have been rejected.

Claims 3, 6, 10, and 14 have been cancelled. No claims have been substantively allowed. The rejection of claims 1-2, 4-5, 7-9, 11-13, 15-17, and 19-20 is being appealed.